

REMARKS

Claims 1-2, 4-7, 9-10, 12-15, and 17-33 are now pending in the application. Claims 18-33 have been added. Support for new claims can be found in the specification as originally filed. Claims 3, 8, 11, and 16 have been cancelled. Claims 1 and 4 have been amended by rewriting claim 3 in independent form as claim 1. Claims 5 and 9 have been amended by rewriting claim 8 in independent form as claim 5. Claims 10 and 12 have been amended by rewriting claim 11 in independent form as claim 10. Claims 13 and 17 have been amended by rewriting claim 16 in independent form as claim 13. The Examiner is respectfully requested to reconsider and withdraw the rejections in view of the amendments and remarks contained herein.

REJECTION UNDER 35 U.S.C. § 103

Claims 1-3 and 5-8 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Zaidi et al. (U.S. Pat. No. 6,601,126) in view of Loafman (U.S. Pub. No. 2005/0021916). Claims 4 and 9 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Zaidi and Loafman as applied to claims 1-3 and 5-8 above, and further in view of Barroso et al. (U.S. Pat. No. 6,725,334). Claims 10-17 are rejected under 35 U.S.C. § 103(a). These rejections are moot as to claims 3, 8, 11, and 16. The rejections as to the remaining claims are respectfully traversed.

With respect to claim 1, Zaidi does not does not show, teach, or suggest “a second memory interface” or “a switch that selectively connects” the first and second memory interfaces to the line cache. Zaidi discloses only a first memory interface, which is the bus 104. With a single memory interface, Zaidi has no need for a switch, as recited by Claim 1. Zaidi discloses an arbiter 142, which does not selectively

connect the line cache to one of the first and second memory interfaces. At most, the arbiter 142 may control the operation of the single memory interface that Zaidi discloses, which is the bus 104.

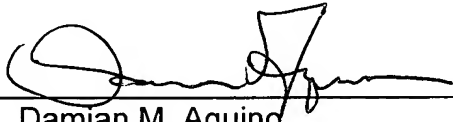
Independent claims 5, 10, 13, 18, and 26 are in condition for allowance for at least similar reasons as claim 1. All remaining claims ultimately depend from these independent claims, which applicant believes to be allowable, and are thus in condition for allowance for at least similar reasons.

CONCLUSION

It is believed that all of the stated grounds of rejection have been properly traversed, accommodated, or rendered moot. Applicant therefore respectfully requests that the Examiner reconsider and withdraw all presently outstanding rejections. It is believed that a full and complete response has been made to the outstanding Office Action and the present application is in condition for allowance. Thus, prompt and favorable consideration of this amendment is respectfully requested. If the Examiner believes that personal communication will expedite prosecution of this application, the Examiner is invited to telephone the undersigned at (248) 641-1600.

Respectfully submitted,

Dated: September 25, 2006

By: 
Damian M. Aquino
Reg. No. 54,964

HARNESS, DICKEY & PIERCE, P.L.C.
P.O. Box 828
Bloomfield Hills, Michigan 48303
(248) 641-1600

MDW/mp